

BIO DATA



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Designation : AP

Department : Electronics and Communication Engineering

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Google Scholar link: [https://scholar.google.com/citations?](https://scholar.google.com/citations?view_op=list_works&hl=en&hl=en&user=8RA00tkAAAAJ)

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1. Educational Qualification

Degree	Branch / Specialization	Institute/University	Year of Passing
Ph.D	Wireless Communication	Thiagarajar College of Engineering	Pursuing
M.E.	VLSI Design	Anna University, Regional Office, Madurai.	2014
B.E.	ECE	Vickram College Of Engineering, Sivagangai.	2010

2. Professional/ Industry Experience

: (in chronological order)

S.No	Designation	Institution/Organization	Period	
			From	To
1.	Assistant Professor	Ramco Institute of Technology, Rajapalayam	08.12.2021	Till Date
2.	Assistant Professor	AAA College of Engineering and Technology, Sivakasi	02.06.2014	06.11.2021
3.	Lecturer	Arulmigu Kallalagar Polytechnic College	13.07.2011	15.08.2012

Updated as on 10.02.2022

3. Research Interest

VLSI Design and Wireless Communication

4. Publications

4.1 JOURNAL PUBLICATIONS

1. **G.Subhashini, V.Vaishalee, K.A.Rajadarwini** Published a paper titled „Comparative Analysis of Full-Adder Using Conventional XNOR and NEO-XNOR Logic“, Journal of Electronic Design Engineering, Print ISSN 2581-639X in Volume 6, Issue 1, Published Online, 2020, Impact Factor:1.05
2. **G.Subhashini, A.Annie Sheryl, R.Vimala** “IOT based Temperature Monitoring System using FPGA” in Journal of Analog and Digital Devices, Volume 4, Issue 3, 2019.
3. **G.Subhashini, J.Mangayarkarasi** “Analytical modeling and characterization of TSV for three dimensional integrated circuits” in Latest Trends in Circuits, Systems, Signal Processing and Automatic Control, Proceedings of 5th International Conference on Circuits, Systems, Control, Signals (CSCS '14) at Salerno Italy held from 4-5th of June, 2014, ISSN: 1790-5117. ISBN: 978-960-474-374- 2 pp 306-313, 2014 **Scopus Indexed Paper.**
4. **G.Subhashini, J.Mangayarkarasi** “Power-delay Co-analysis of TSV for three dimensional integrated circuits” in CIIT International Journal of Programmable Device Circuits and Systems, vol. 6, no. 2, April 2014.

4.2 CONFERENCE PUBLICATIONS

1. **G.subhashini (2021)**, “Development of Gyro Stabilized Electro –Bot” in Virtual International Conference on Interdisciplinary Research in Social Sciences, Sciences and Technology held at KG College of Arts and Science , Coimbatore on 21.05.2021 and 22.05.2021.
2. **G.subhashini (2021)**, “Design of Low Power and high Speed Hybrid Multiplier” in Seventh International Conference on Emerging Trends in Engineering and Technology (ICETET‘20) held at Pandiyan Saraswathi Yadav Engineering College on 13.03.2020 & 14.03.2020
3. **G.subhashini (2020)**, “A High Speed and Low Power Multiplier using a Redundant Binary Booth Encoding” in Seventh International Conference on Emerging Trends in Engineering and Technology (ICETET‘20) held at Pandiyan Saraswathi Yadav Engineering College on 13.03.2020 & 14.03.2020.
4. **G.subhashini (2019)**, “IOT based temperature monitoring System using FPGA” in 4th International Conference on Technical Innovations in Electrical, Electronics, Computer and Communications organized by PET Engineering College.
5. **G.subhashini (2019)**, “Implementation of Area, Power, Speed efficient adder using FPGA” in 4th International Conference on Technical Innovations in Electrical, Electronics, Computer and Communications organized by PET Engineering College.
6. **G.subhashini (2017)**, “Analysis of Three Dimensional IC with TSV” in International Conference on Recent Trends in Electrical, Electronics and Communication Engineering organized by P.S.R Engineering College.
7. **G.subhashini (2017)**, “Configuration of NEO full adder” in International Conference on Technical Innovations in Electrical, Electronics, Computer and Communications organized by PET Engineering College.

8. **G.subhashini (2014)**, “Analytical modeling and characterization of TSV for three dimensional integrated circuits” in the National Conference on microwave and optical communication organized by Alagappa Chettiar College of Engineering and Technology.
9. **G.subhashini (2014)**, “Field Effect Transistors Power-delay co- analysis of TSV for Three Dimensional Integrated Circuits” First International Conference on VLSI Computation, Networks, Drives, System Design conducted by Sethu Institute of Technology.

4.3 Book/Book chapter publications

- I have written the book for the subject of **Radar and Navigational Aids** for 7th semester in suchithra publication for 2013 regulation.

5. Funded Projects

S.No.	Title of the project	Funding Agency	Duration	Amount	Status
-	-	-	-	-	-

6. Ph.D. guidance

Sl.No	Scholar name	University	Topic	Status
-	-	-	-	-

7. Short term Courses / Seminars /FDP/Workshop/ConferenceAttended:

Sl.No	Title of the event	Organized by	Date	
			From	To
1.	One Week International Workshop on “Machine Learning & Its Applications”	Indira Gandhi National Tribal University, Amarkantak	24.01.2022	28.01.2022
2.	Seminar on “Funding Opportunities”	Kalasalingam Academy of Research and Education	21.01.2022	-
3.	FDP on “Recent Developments in IoT”	AICTE.	17.01.2022	21.01.2022
4.	Seminar on Immunizing Cyber Security	University College of Engineering Thirukkuvilai	12.01.2022	-
5.	2nd International Conference on New Computer Science and Engineering Trends (NCSET2021)	World Advanced Science	30.12.2021	-
6.	Foundations of Data science and Machine learning for Cross Domain Applications	ATAL FDP PSG College of Technology, Coimbatore	27.12.2021	31.12.2021
7.	FDP on “Cognitive Load Theory and Application”	National Institute of Technology Durgapur	21.12.2021	25.12.2021

8.	Innovation Ambassador training (Foundation Level)	MoE's Innovation Cell & AICTE	30.06.2021	30.07.2021
9.	Anna University six Days FDTP program on Discrete Time Signal Processing	Department of ECE, K.L.N.College of Engineering	31.05.2021	05.06.2021
10.	short term training program on “Internet of things Based Automation using Machine Learning”	Hindustan College of Engineering and Technology	19.04.2021	24.04.2021
11.	Online FDP on "Robotics"	ATAL FDP	08.12.2020	12.12.2020
12.	STTP on Cyber Security	P.S.R Engineering College	23.11.2020	28.11.2020
13.	FDP on Recent trends in Computer Architecture, VLSI and Embedded Systems	Department of ECE, GRIET	08.06.2020	13.06.2020
14.	Analog VLSI Design Flow Specs to Final Product from Industry Perspective	IEI ECE Student Chapter, GRIET	07.06.2020	-
15.	Modern Antenna Design Challenges	Department of ECE, Kings College of Engineering	06.06.2020	-
16.	FDP on Recent Trends in VLSI	Department of ECE, GRIET	02.06.2020	07.06.2020
17.	Seminar on “Satellite Technologies and it’s applications”	Mountzion College of Engineering and Technology	31.05.2020	-
18.	Accelerating Industry 4.0 - A Post Covid Paradigm	ICTCADEMY	25.05.2020	29.05.2020
19.	Education, Employment & Entrepreneurship	ICTCADEMY	11.05.2020	15.05.2020
20.	Faculty Development Program on eSim	Department of ECE, K.L.N. College of Engineering,	06.05.2020	08.05.2020
21.	The Future of Education, Employment & Entrepreneurship	ICTCADEMY	04.05.2020	08.05.2020
22.	Examination Reforms	AICTE.	29.04.2020	02.05.2020
23.	FDP on “Moodle Learning Management System”	Department of Computer Science, Kaypeeyes College of Arts & Science, Kotagiri,	29.04.2020	01.05.2020
24.	The Future is here now	ICTCADEMY	27.04.2020	31.04.2020
25.	Theme Leading in the VUCA world	ICTCADEMY	20.04.2020	24.04.2020
26.	FDP on “Industrial Internet of Things”	AAA College of Engineering and Technology	16.01.2019	17.01.2019
27.	Short term training program on “Advanced Verilog Programming”	Learn Bench India, Chennai	14.06.2017	01.07.2017

28.	STTP on “CMOS Mixed Signal and Radio Frequency VLSI design	IIT Karagpur	30.01.2017	04.02.2017
29.	workshop on “Introduction to Robotics”	Eyantra held at Mepco Schlenk Engineering College	16.03.2015	17.03.2015
30.	workshop on “Introduction to Robotics”	Eyantra held at Mepco Schlenk Engineering College	29.06.2015	30.06.2015
31.	Faculty Development Training Programme on VLSI Design	National Engineering College	01.12.2015	08.12.2015
32.	Faculty Development Training Programme on Electronic Circuits-I	Anna University Chennai	23.06.2014	28.06.2014
33.	Seminar on OrCAD PSPICE & OrCAD PCB	Entuple Technologies Pvt. Ltd.	22.06.2015	28.06.2015

8. Short term Courses / Seminars /FDP/Workshop/Conference organized:

Sl.No	Title of the event	Organized by	Date	
			From	To
1.	SCIENCE DAY 2021	Institutions Innovation Council AAA College of Engineering and Technology	12.03.2021	-
2.	Innovative idea Competition-ECE Creative Minds 2021	Institutions Innovation Council AAA College of Engineering and Technology	22.01.2021	-
3.	An Experimental Model Display Contest: TECHNOFEST-2021	Institutions Innovation Council AAA College of Engineering and Technology	22.01.2021	-
4.	Students Association Inauguration 2021-2022	Department of ECE, AAA College of Engineering and Technology	22.01.2021	-
5.	National Level Technical Symposium VALIANT 2020	Department of ECE, AAA College of Engineering and Technology	31.01.2020	01.02.2020
6.	Five Days Workshop on “Salesforce ADX-201”	Department of ECE, AAA College of Engineering and Technology	25.11.2019	30.11.2019
7.	Two day workshop on “Mobile Controlled Robotics”	Department of ECE, AAA College of Engineering and Technology	07.03.2016	08.03.2016
8.	Two day workshop on “Quadcopter development workshop cum championship”	Department of ECE, AAA College of Engineering and Technology	17.08.2016	18.08.2016

9. Online Courses

Sl.No	Course title	University	Date
1.	Structuring your article correctly	Researcher Acahemy	07.02.2022
2.	Machine Learning for all	University of London	18.01.2022
3.	MATLAB Fundamentals	Mathworks	18.12.2021
4.	MATLAB Onramp	Mathworks	09.12.2021
5.	Internet of Things and Embedded Systems	University of California, Irvine	29.06.2020
6.	CMOS VLSI Design	IIT Guhawati	2020-2021
7.	Digital Circuits	IIT Kharagpur	2019-2020

10. Patents filed/ awarded:

11. Membership in Professional societies:

- The Society of Digital Information and Wireless Communications

12. Awards, Recognition& Achievements:

Any others (Board of studies/Governing Council/Staff Selection/Reviewer/other duties)

Awards: I have participated in “**e-yantra Task Based Training Program**” and won **Class C**

Award with Cash Prize from IIT Roorkee

Achievements: Graduated with 44th **Anna University Rank** in the Degree of Master of Engineering in **VLSI DESIGN** programme under the Regional office of Anna University, Madurai.

Acted as a mentor for students in DST and Texas Instruments Innovation Challenge Design Contest 2019 powered by AICTE and anchored by NRCEL@IIMB

Other Responsibilities:

Responsibilities in Ramco Institute of Technology

- ISO Department Coordinator
- Faculty Online Course In-Charge

Responsibilities in AAA College of Engineering:

- IIC Ambassador
- IIC Coordinator
- Timetable Coordinator
- News Letter Coordinator
- Association Coordinator
- Class Advisor
- Proctor

