# RAMCO INSTITUTE OF TECHNOLOGY

Approved by AICTE, New Delhi & Affiliated to Anna University

NAAC Accredited with 'A+' Grade & An ISO 9001: 2015 Certified Institution

NBA Accredited UG Programs: CSE, EEE, ECE and MECH

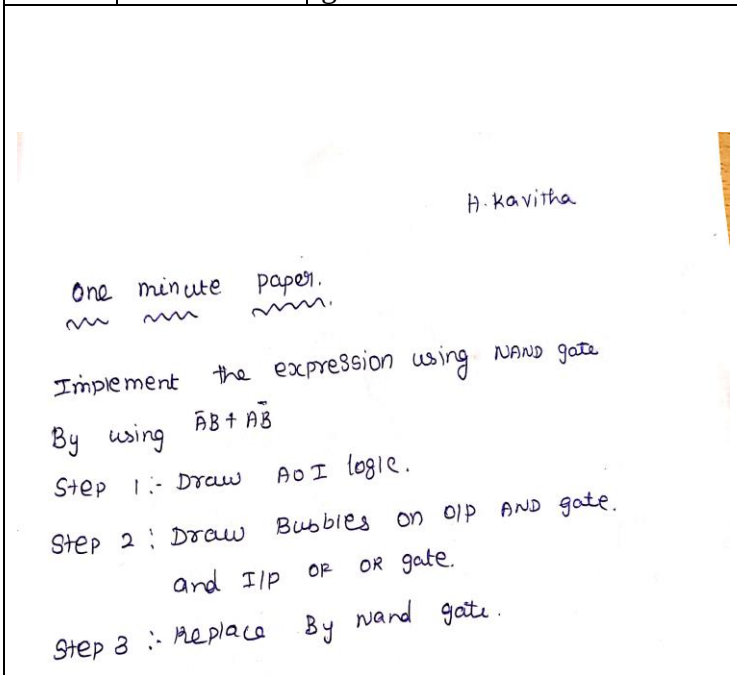
**Department of Electronics and Communication Engineering**

**Academic Year: 2024 – 2025(ODD Semester)**

## INNOVATIVE TEACHING METHODS

**Degree, Semester & Branch : III Semester B.E. ECE A**
**Course Code & Title : EC3352 & DIGITAL SYSTEM DESIGN**
**Name of the Faculty member: Mrs.V.Krishna Meera**

| Sl.No. | Date | Topic(s) | Activity* | Reference |
|---|---|---|---|---|
| **UNIT I BASIC CONCEPTS** | | | | |
| 1. | 21.08.2023 | Implementation of Boolean expressions using universal gates | One Minute Paper | https://oncourseworkshop.com/self-awareness/one-minute-paper/ |



A. Kavitha

One minute paper.

Implement the expression using NAND gate

By using $\bar{A}B + A\bar{B}$

Step 1 :- Draw AOI logic.

Step 2 : Draw Bubbles on o/p AND gate.
and I/p OR OR gate.

Step 3 :- Replace By nand gate.

**Signature of the Faculty Member** **HOD**

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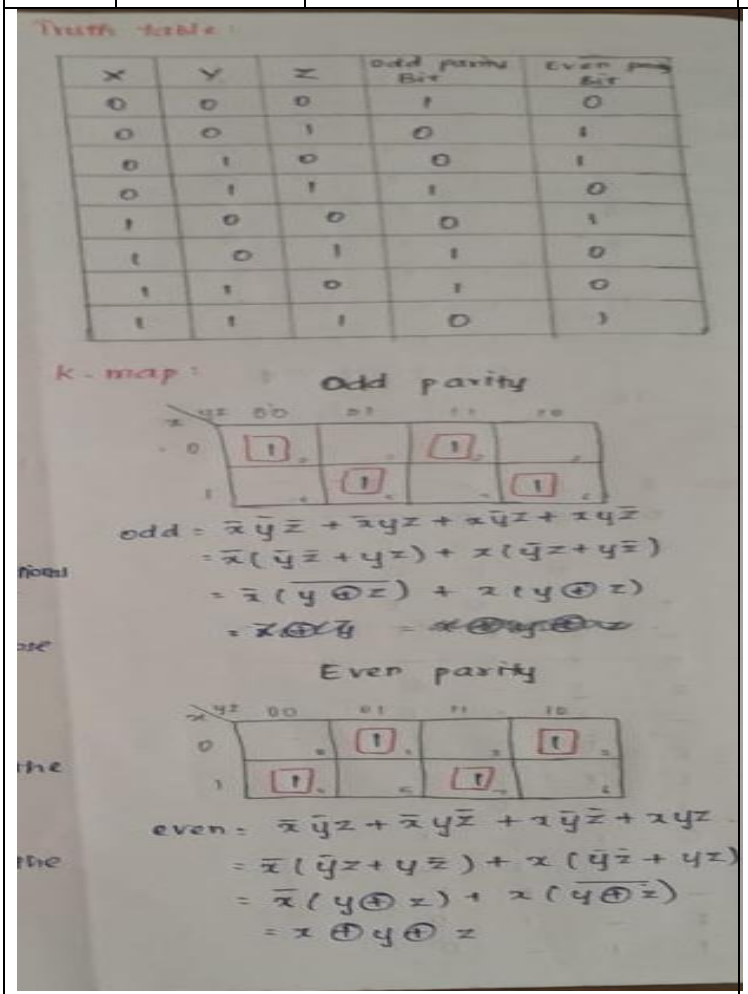
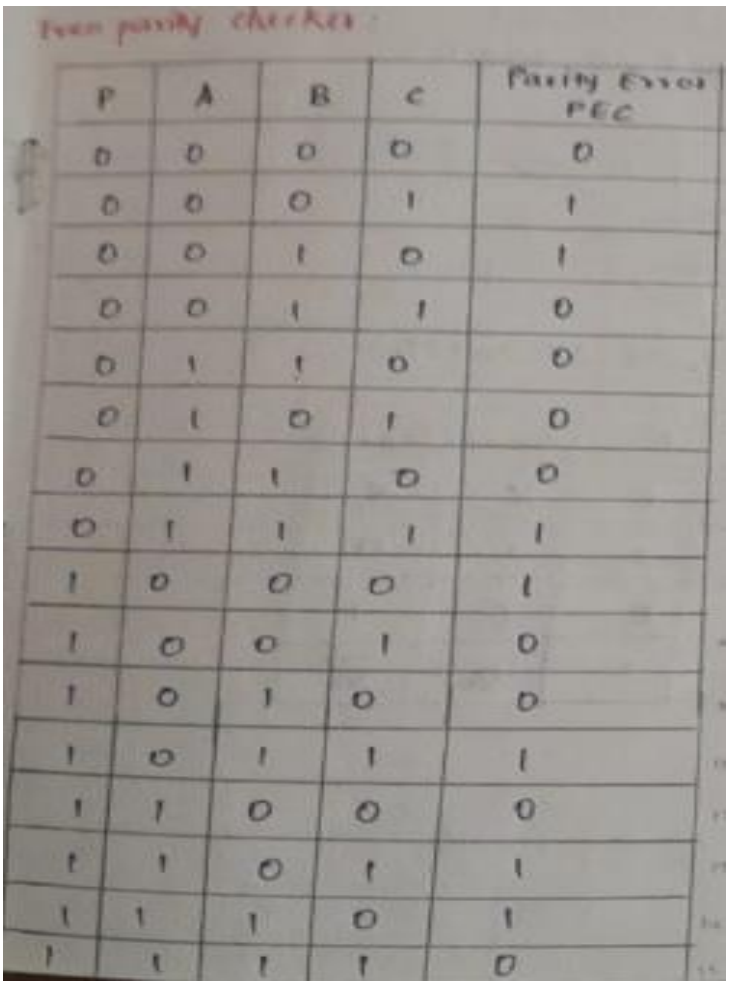
| **UNIT II COMBINATIONAL LOGIC CIRCUITS** | | | | |
| 1. | 10.09.2023 | Parity Generator/Checker, Seven Segment display decoder | Note Check | https://www.niu.edu/citl/resources/guides/active-learning-activities.shtml#:~:text=Note%20Check,clarification%20on%20a%20course%20concept. |



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