

ABOUT THE COLLEGE

Ramco Institute of Technology is founded by Shri P.R.Ramasubrahmaneya Rajha, Chairman, Ramco Group in 2013 with a vision to impart high quality engineering education. It is situated in the foothills of Western Ghats, 5 Kms from Rajapalayam Bus stand in Ayyanar Kovil Road. The College provides a world-class infrastructure and facilities to both our students and staff members. It has a very good learning atmosphere facilitated by high quality teaching by well-qualified and experienced faculty members, e-learning modules with smart classrooms, emulating IIT system of education.

ABOUT THE DEPARTMENT

The Department of ECE and EEE are established with a vision to develop competent professionals to meet the global challenges in modern engineering society. At present, it offers a 4-year B.E. ECE & EEE programme with a sanctioned intake of 120 and 60 students respectively. The laboratories are equipped with latest technology in the field of Electronic Devices & Circuits, Digital Electronics, Linear Integrated Circuits, Microprocessor and Microcontroller, Communication Systems, Digital Signal Processing, Embedded Systems, Electrical Machines, Power Electronics, Control & Instrumentation, Power System Simulation that offers opportunities to work on a wide range of advanced software packages. It has a team of committed faculty members and encompasses professional associations such as Students Technical Association "RIT Electronica" and "RIT Electra", IETE, ISTE chapters which provide the students, a platform to learn professional and soft skills.

PROGRAMME OBJECTIVE

The objective of the Programme is to provide a platform for sharing and disseminating knowledge about Signals and Systems. This programme aims at imparting indepth knowledge in this course. The laboratory sessions using NI LabVIEW and MATLAB will equip the academicians to carry out projects. To provide ideas in recent trends and developments in the area of signal processing.

PROGRAMME CONTENTS

The following topics will be covered

- Classification of Signals and Systems
- Analysis of Continuous Time Signals
- Fourier Transforms in CT Signal Analysis
- Analysis of systems using Laplace Transforms
- Fourier Series
- Linear Time Invariant- Continuous Time Systems
- Impulse response
- Convolution Integrals
- Discrete Fourier and Z Transform Analysis of Recursive systems and Non-Recursive Systems
- Applications of transforms in signal, image and speech processing
- Hands on training using NI LabVIEW and MATLAB

RESOURCE PERSONS

Experienced faculties from premier Institutions/ Industrial experts will deliver the lectures and handle practical sessions.

5-DAYS FACULTY DEVELOPMENT TRAINING PROGRAMME on Signals and Systems

13th - 18th JUNE 2018

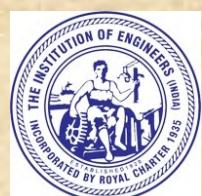
Organized by



**DEPARTMENT OF ECE & EEE
RAMCO INSTITUTE OF TECHNOLOGY**

**North Venganallur Village
Rajapalayam - 626 117.
Phone : 04563 - 233414, 233413**

in Association with
IETE & IE(I)



ACCOMMODATION

On prior request, accommodation may be provided for outstation candidates in our college hostel subject to availability with breakfast and dinner at a nominal fee.

WHO CAN REGISTER

Preference will be given to young and dynamic faculties who are working in colleges affiliated to Anna University.

Selection will be made on First come First serve basis and is limited to thirty.

If selected, faculties should attend the training programme for all days.

REGISTRATION FEE

Faculty Members / Research Scholars from Academic Institutions - Rs.500/-

The registration fee should be paid through Demand Draft drawn in favour of **"RIT-Electronica, Ramco Institute of Technology"**, payable at Rajapalayam.

HOW TO APPLY

The applicants should send their registration form in the specified format along with DD on or before 07.06.2018. Selected candidates will be intimated through e-mail only. Soft copy of the filled-in application can also be e-mailed to ritecepgm@ritrjpm.ac.in.

FOR FURTHER INFORMATION CONTACT

The Co-ordinators,
Department of ECE & EEE,
Ramco Institute of Technology,
Rajapalayam- 626 117.
Email: ritecepgm@ritrjpm.ac.in
Phone: 04563-233414, 233413
Mobile No.: +91-9159814661, 9750588231

FDTP ON SIGNALS AND SYSTEMS

13th - 18th JUNE 2018

REGISTRATION FORM

Name : _____

Age & Gender : _____

Qualification : _____

Designation : _____

Department : _____

Institution : _____

Address for Communication: _____

Professional Experience: _____

Teaching:.....years, Industry:.....years

Contact No. : _____

E-mail ID : _____

Accommodation Required: Yes / No.

I hereby declare that the above furnished information is true to the best of my knowledge and I will abide by the rules and regulations of FDTP in time. If selected, I will attend the program in full.

**Signature of the Participant with Date
Forwarded and Recommended**

**Signature & Seal
Head of the Department/Institution**

ORGANIZING COMMITTEE

CHIEF PATRON

**Shri. P.R. Venketrama Raja
Chairman, Ramco Group**

PATRON

**Dr.S.Rajakarunakaran
Principal
Ramco Institute of Technology**

CONVENORS

**Dr.S.Kannan
Professor & Head/EEE**

**Dr.S.Periyanayagi
Associate Professor/ECE**

CO-ORDINATORS

**Dr.B.Deepa Lakshmi
Assistant Professor (SG)/ECE**

**Mrs.M.Sangeetha
Assistant Professor/ECE**

**Mr.D.Karthik Prabhu
Assistant Professor (SG)/EEE**

VENUE

**Seminar Hall
Department of ECE**

IMPORTANT DATES

**Submission of Application : 07.06.2018
Intimation of Selection : 09.06.2018
Confirmation by Participants:11.06.2018**